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EFS ID:	3065177			
Application Number:	10631921			
International Application Number:				
Confirmation Number:	9664			
Title of Invention:	TRANSISTOR GATE AND LOCAL INTERCONNECT			
First Named Inventor/Applicant Name:	Jigish D. Trivedi			
Customer Number:	20995			
Filer:	James W. Ausley/Wayne Mahoney			
Filer Authorized By:	James W. Ausley			
Attorney Docket Number:	MICRON.104DV1			
Receipt Date:	28-MAR-2008			
Filing Date:	31-JUL-2003			
Time Stamp:	14:08:06			
Application Type:	Utility under 35 USC 111(a)			

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Request for Certificate of Correction	Certificate_of_Correction_MI CRON-104DV1.pdf	61214	no	2				
		cd085284a74009e476fc7dc69c19fa86d 9008d87						
Warnings:								
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2 Fee Worksheet (PTO-06)	fee-info.pdf	8150		0				
		2ca562a8164b3f8637601976f8901fa9c d28c26f	no	2				
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